

FIG. 1

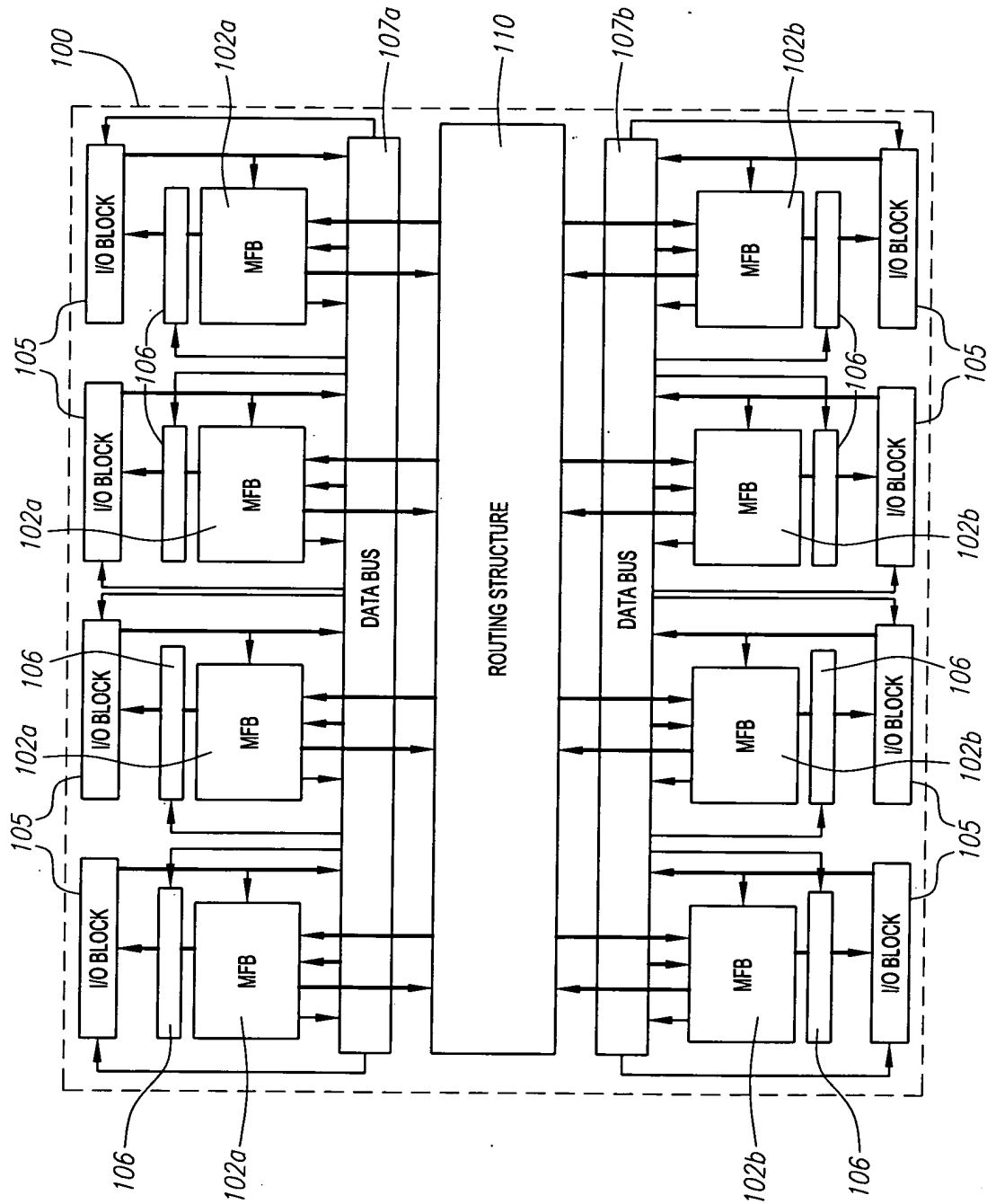


FIG. 2A-1

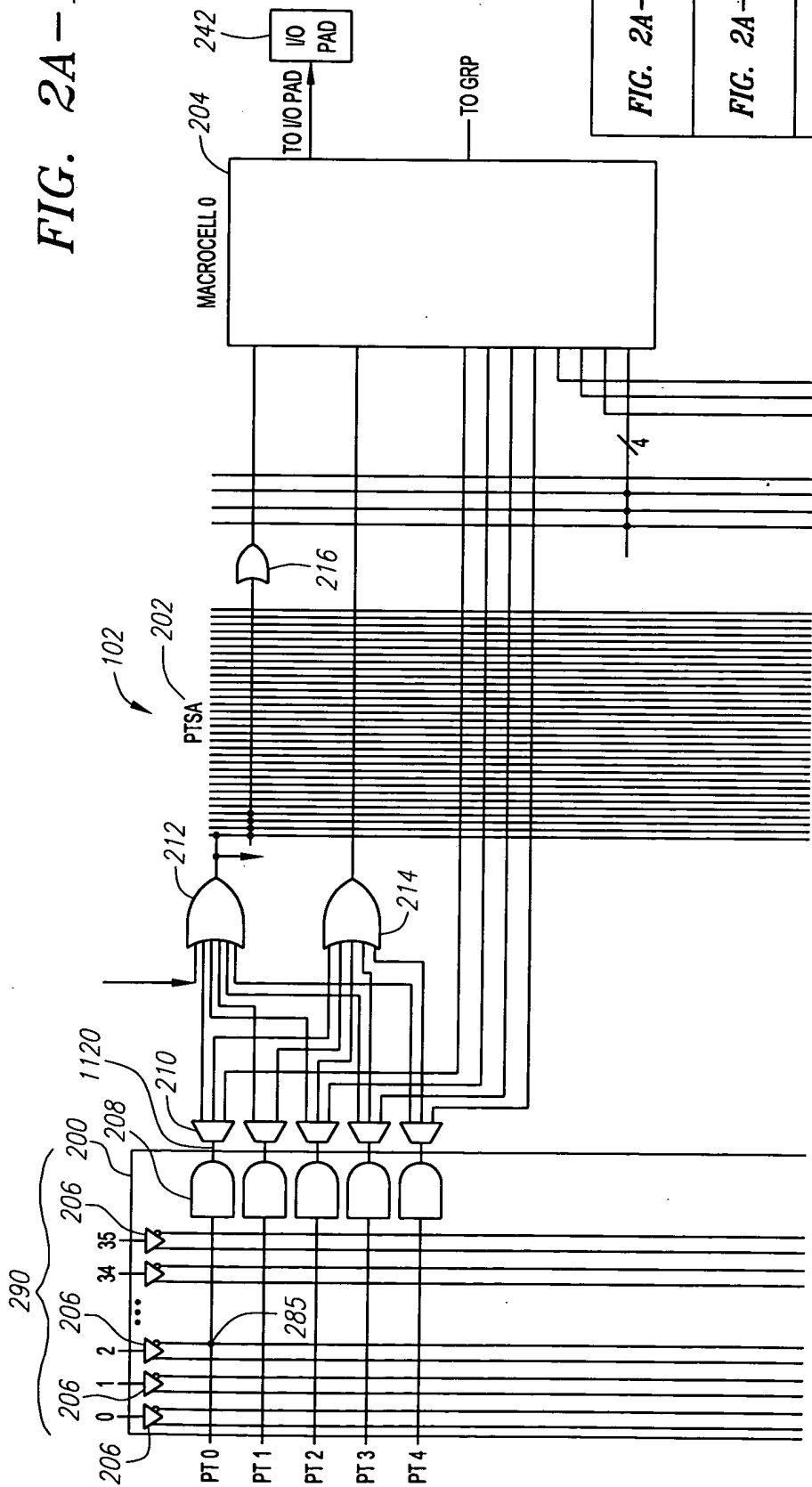


FIG. 2A

FIG. 2A-1
FIG. 2A-2
FIG. 2A-3
FIG. 2A-4

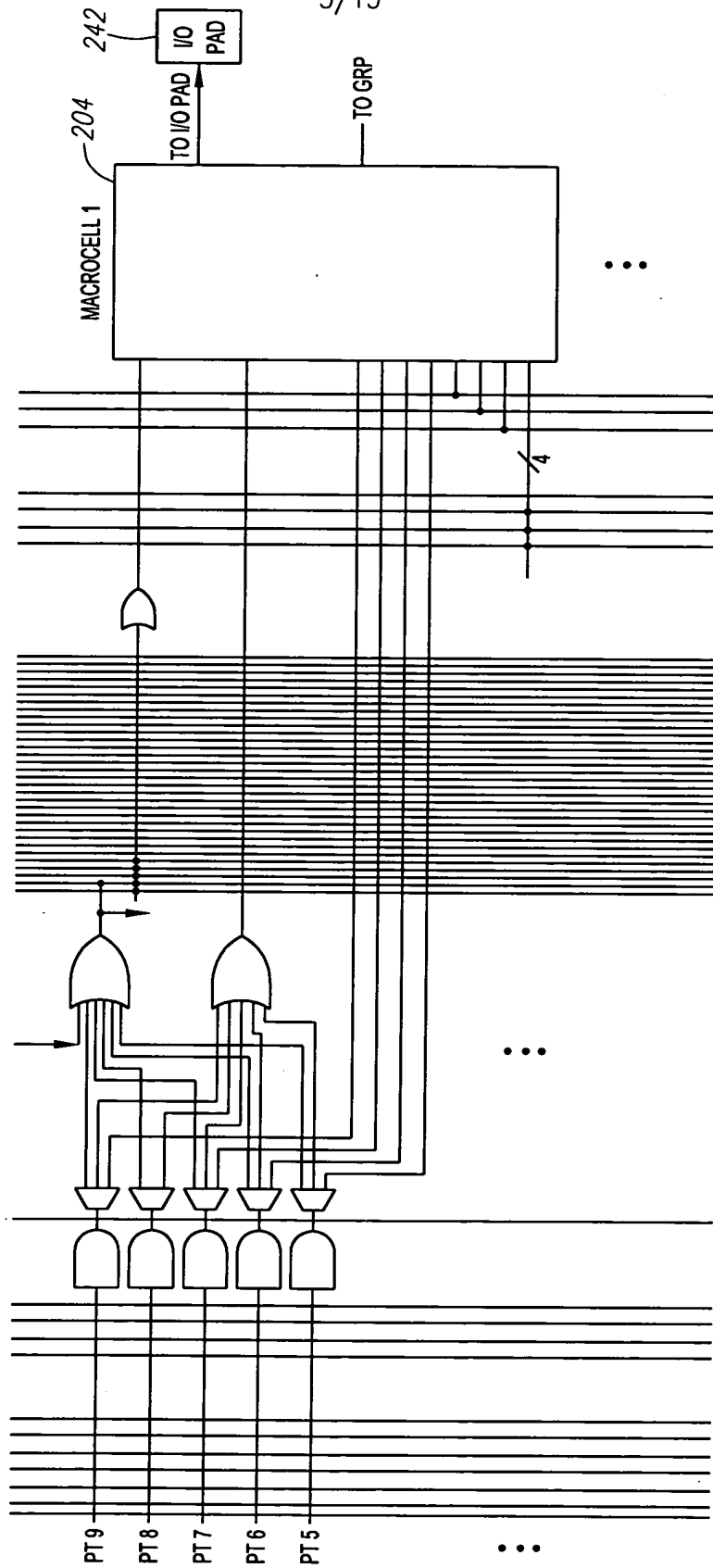


FIG. 2A-2

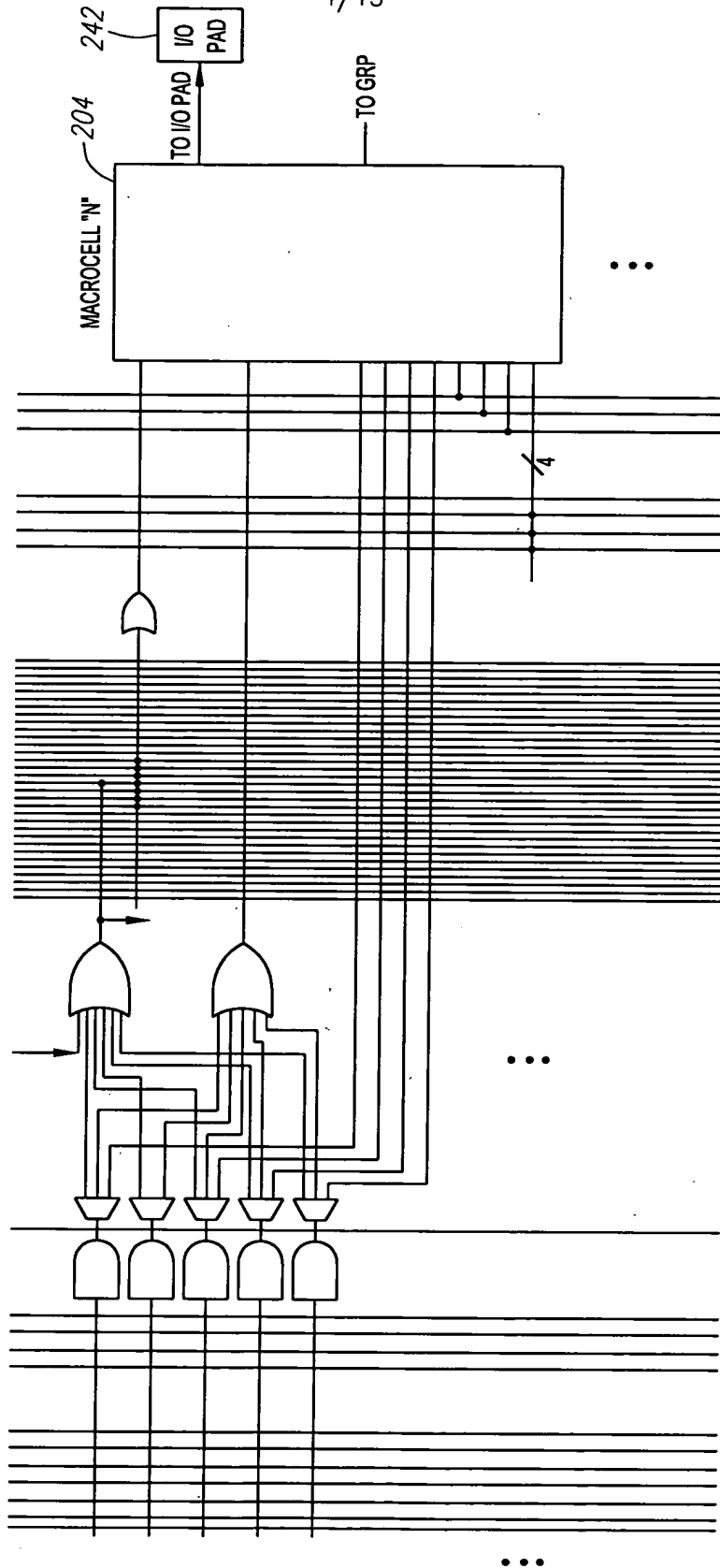
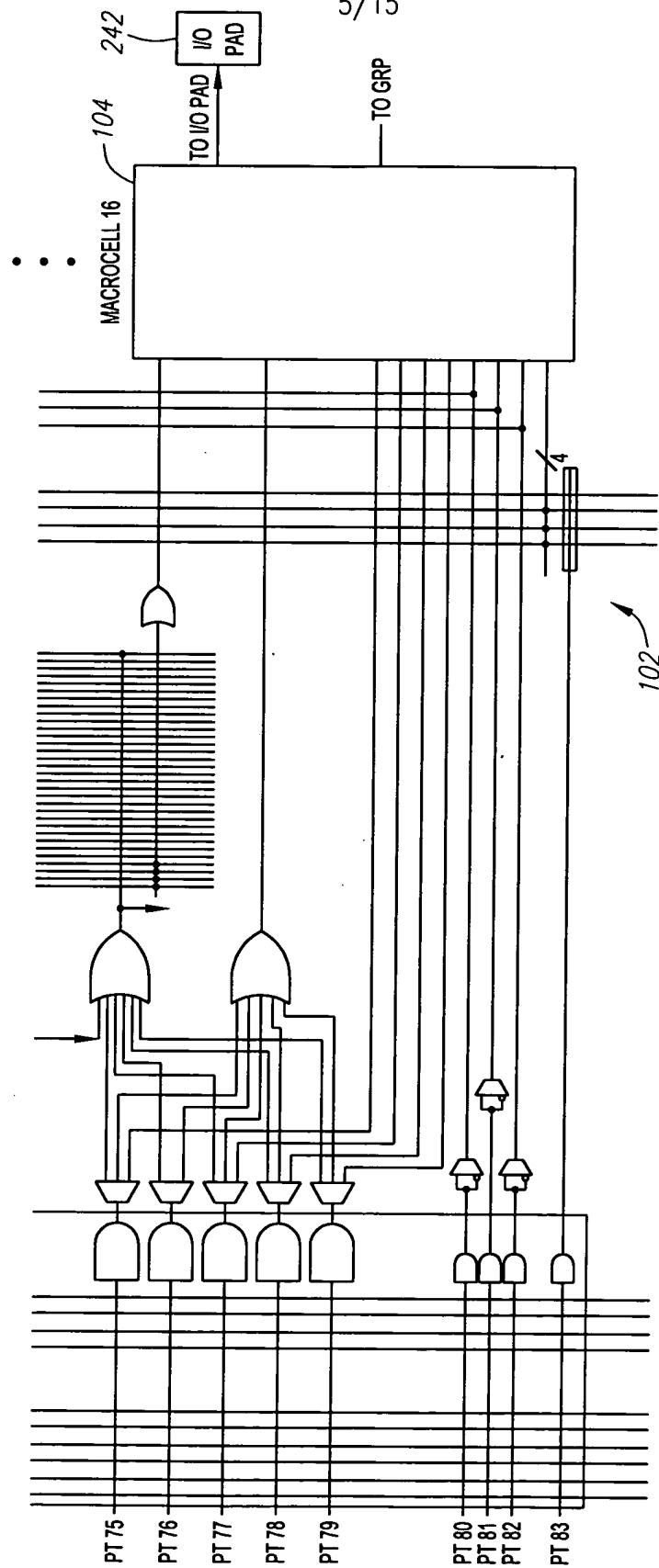


FIG. 2A-3



PROGRAMMABLE LOGIC DEVICE WITH ENHANCED
WIDE INPUT PRODUCT TERM CASCADING

INV(s): J. CHENG, et al.
DKT. NO.: M-9559-2P US

6/15

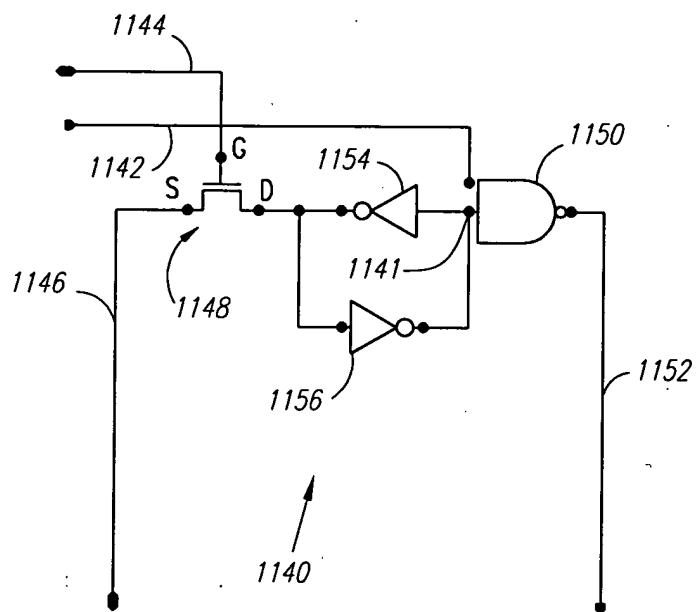


FIG. 3

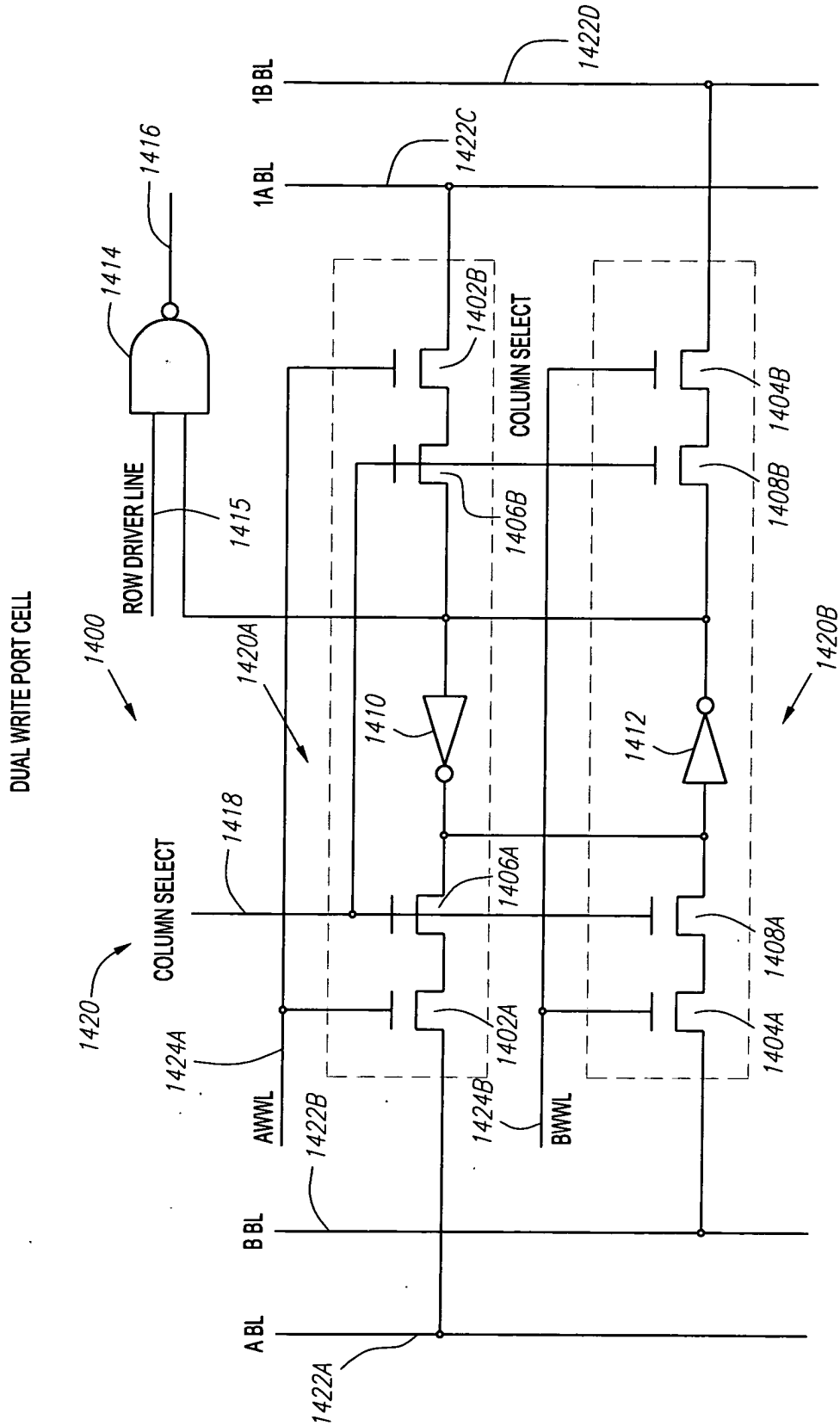


FIG. 4

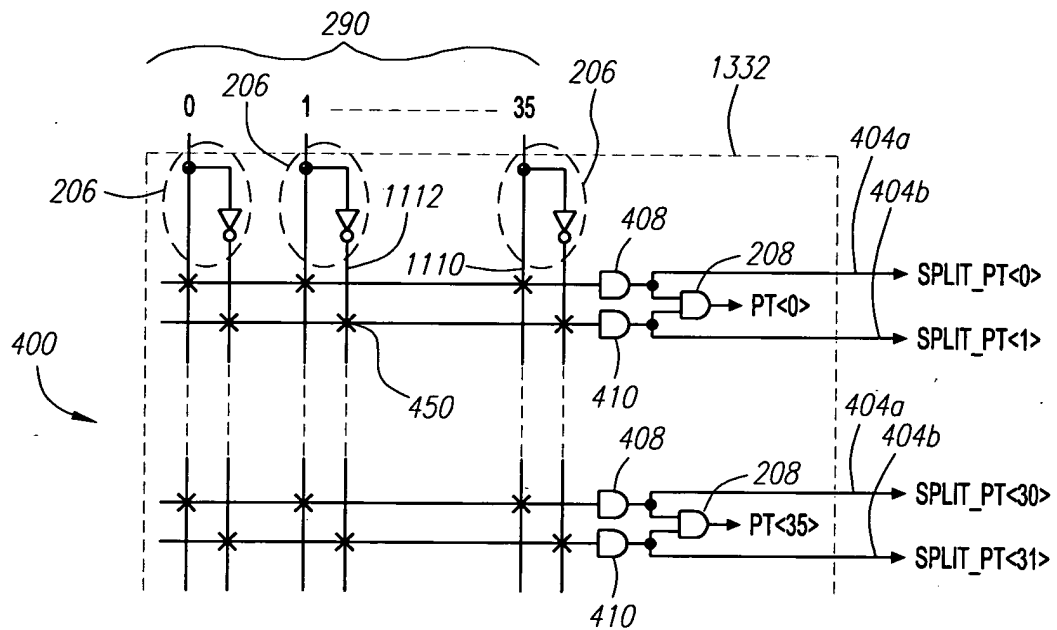


FIG. 5B

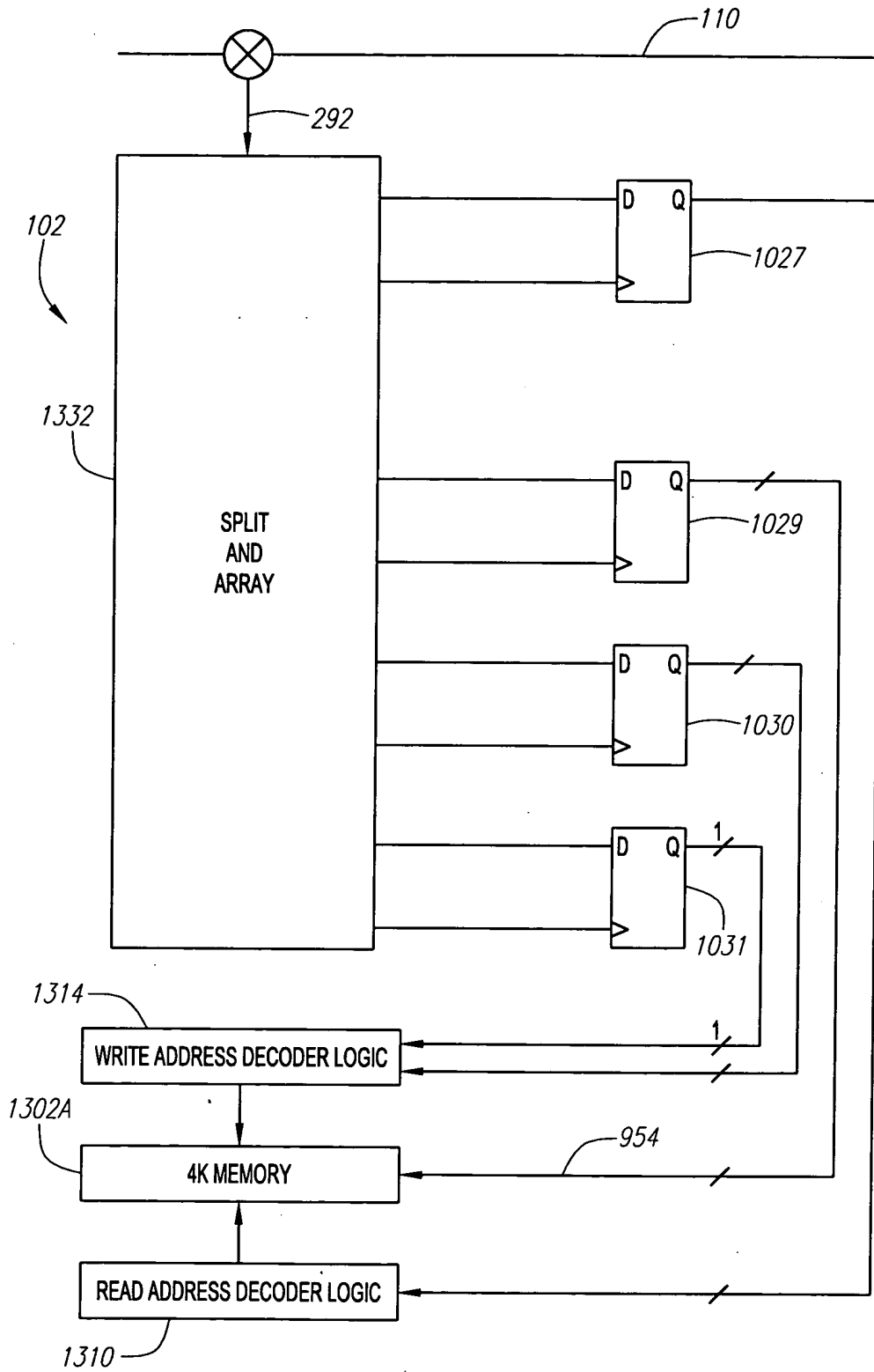


FIG. 6

11/15

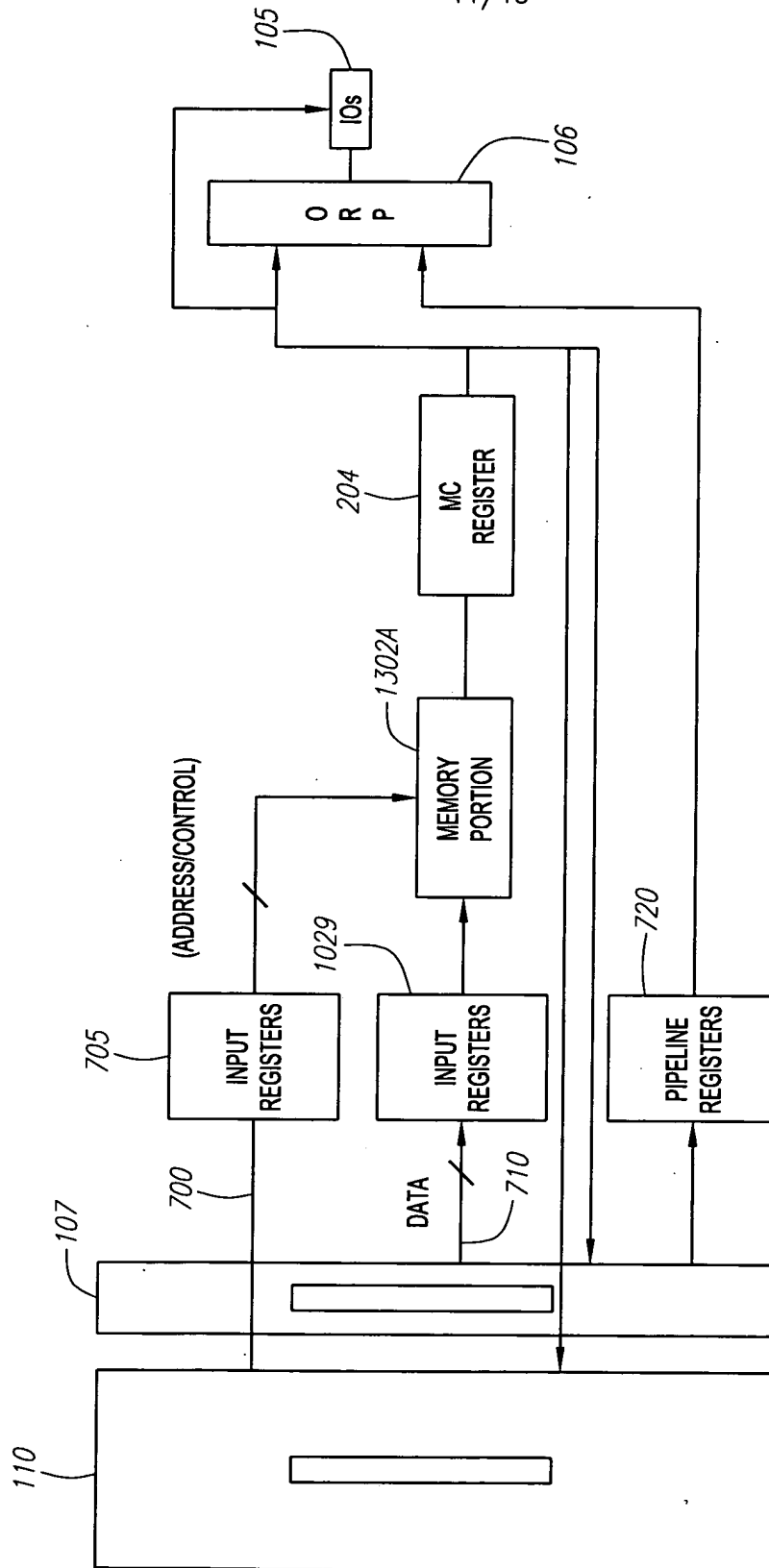


FIG. 7

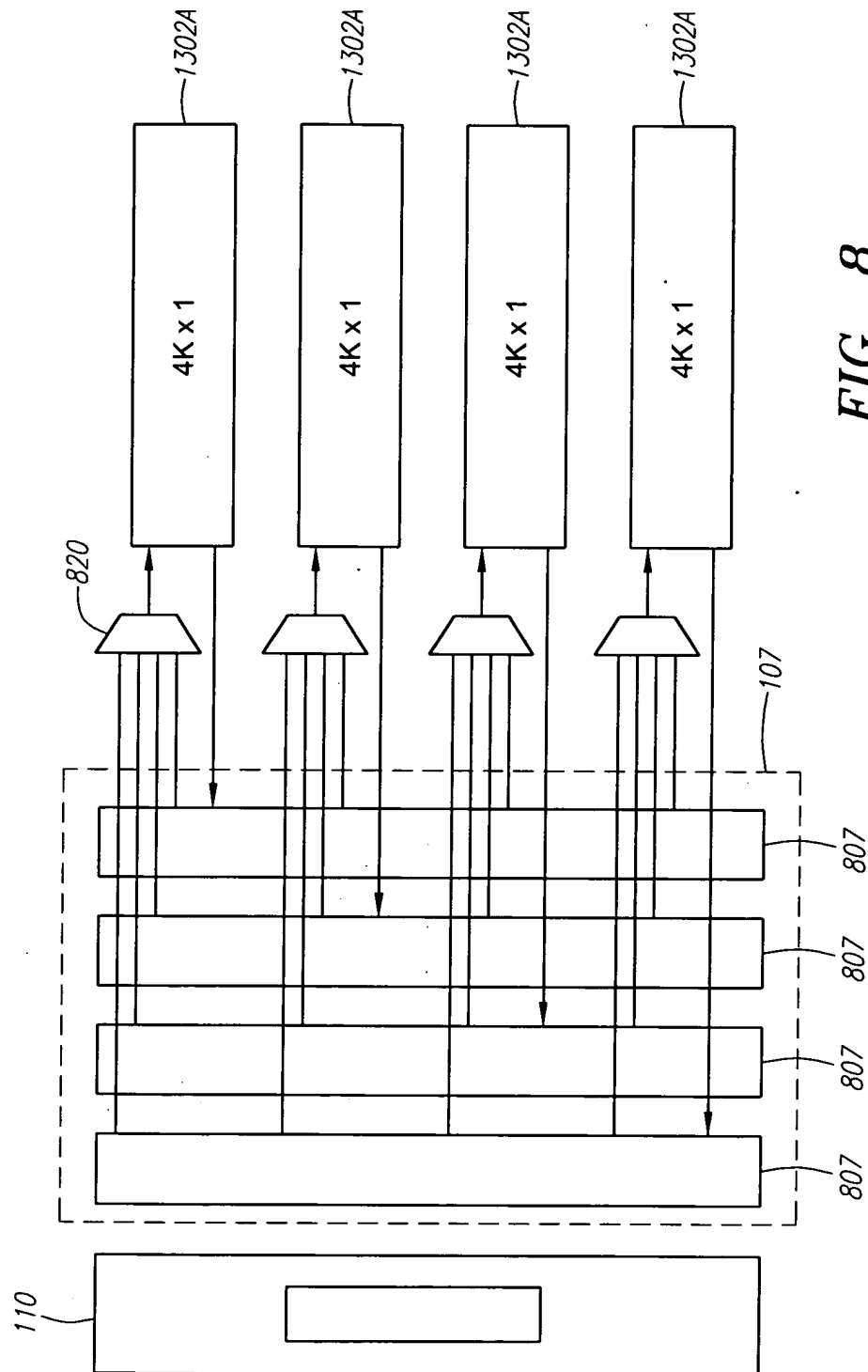


FIG. 8

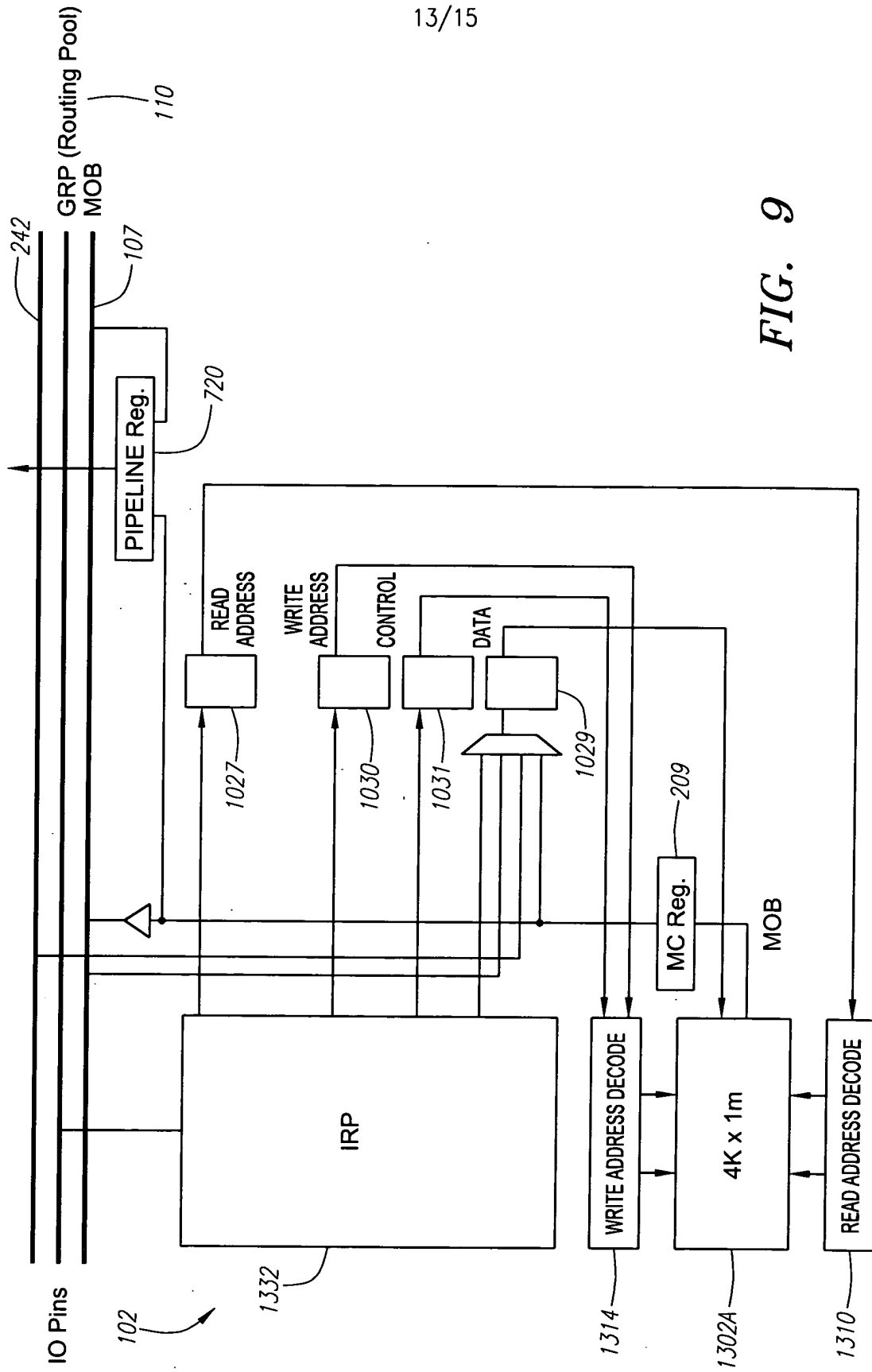


FIG. 9

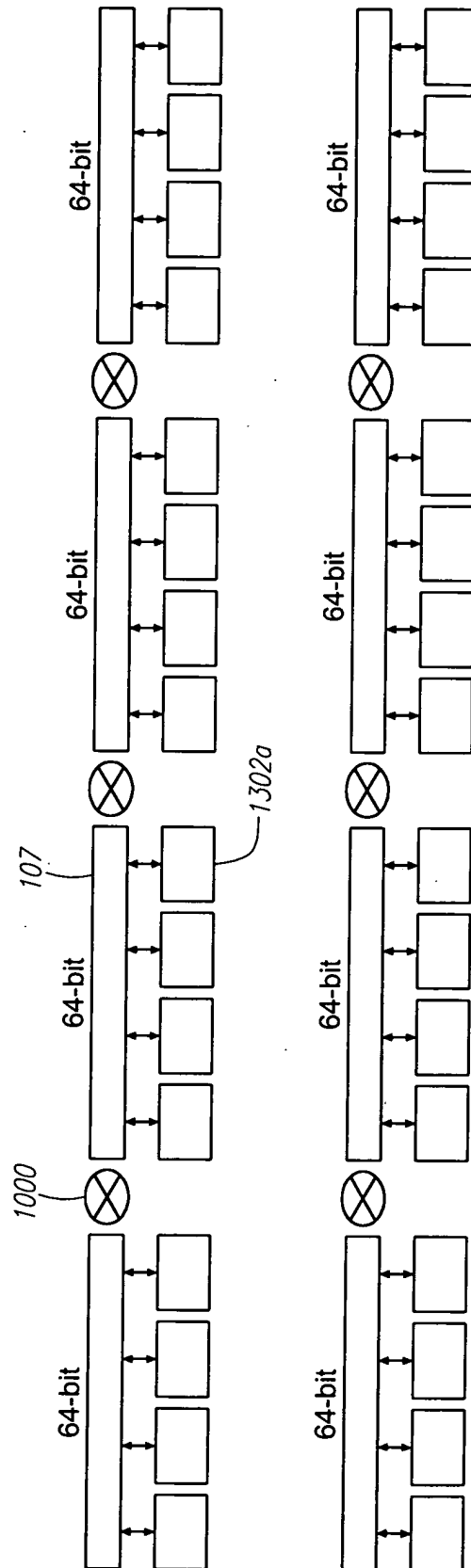


FIG. 10

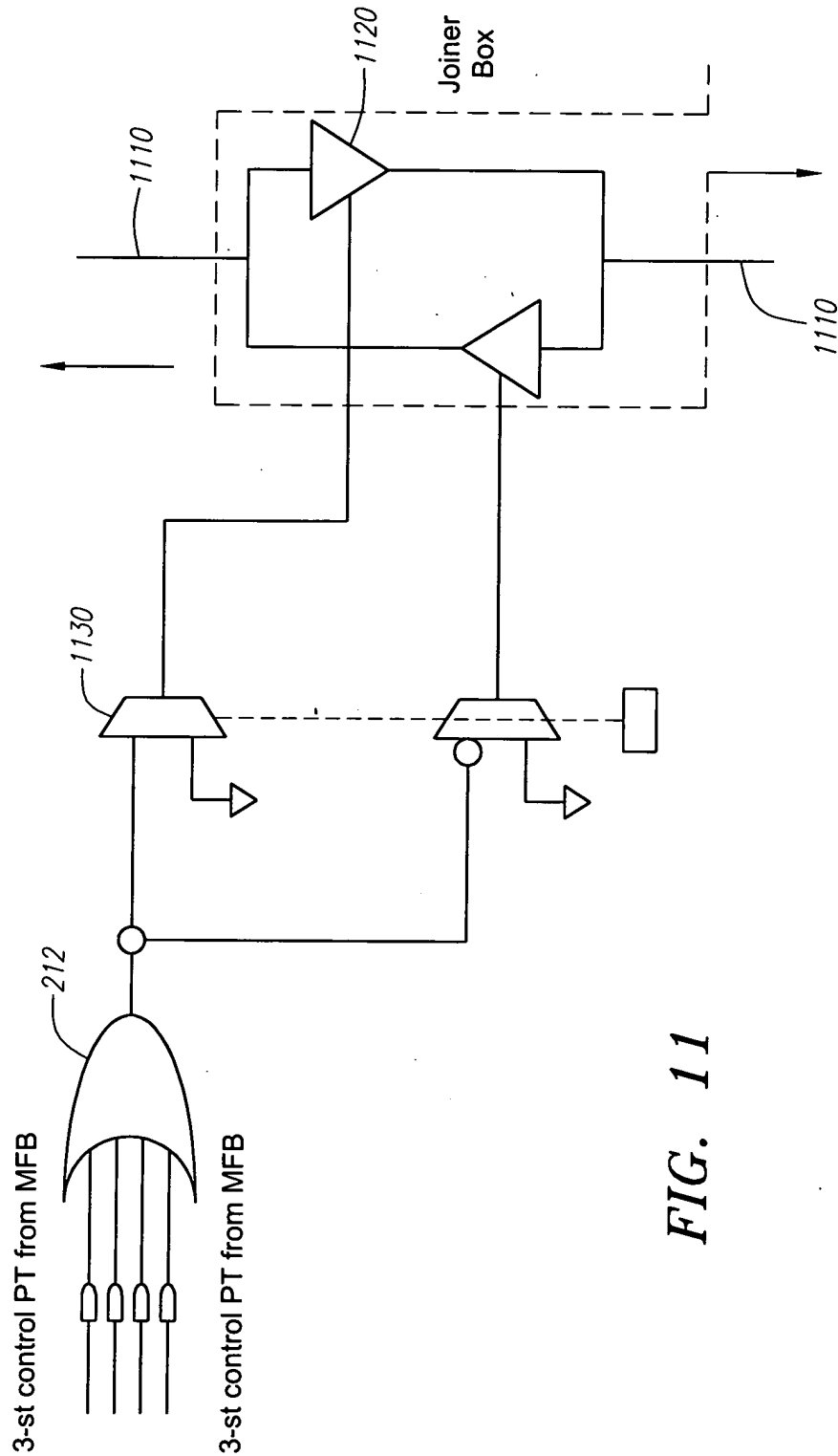


FIG. 11